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Karen Vertz	(Depositor's name)
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May 17 2006	(Date)
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	APP EIOATION III.				T1-36626		4149		
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TITLE OF INV	ENTION:	METHOD OF SEMIC	ONDUCTOR DEVICE	ASSEA	IBLY INCLUDING	FATIGUE-RESISTA	NT TER	NARY SOLDER ALLOY	
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Texas Instruments Incorporated Facsimile: 972-917-4418

# IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

**Applicant** 

Kejun Zeng

Docket Number: TI-36626

Serial No.: 10/797,779

Art Unit: 1725

Filed: 03/09/04

Examiner: Lynne R. Edmondson

For:

Method of Semiconductor Device Assembly Including

Fatigue-Resistant Ternary Solder Alloy

## CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that the following papers are being transmitted by facsimile to the U.S. Patent and Trademark Office on the date shown below:

Kanen Vertz

5-17-06

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NAME OF INVENTOR(S):		RECEIPT DATE & SERIAL NO.:				
Kejun Zeng		Serial No.: 10/797,779				
TITLE OF INVENTION: Meth Semiconductor De Including Fatigue-Solder Alloy	evice Assembly	Filing Date: 03/09/04				
TI FILE NO.:	DEPOSIT ACCT. NO.:					
TI-36626	20-0668					
FAXED: 5-17-06 DUE: 7-24-06 ATTY/SECY: GCH/kjv						

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